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PATENT NUMBER and
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U.S. UTILITY Patent Application

APPL NUM 10030104	FILING DATE 05/23/2002	CLASS 324	SUBCLASS 658	GAU 2858	EXAMINER W. Benson
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****APPLICANTS:** Yamaoka Shuji;

****CONTINUING DATA VERIFIED:**
THIS APPLICATION IS A 371 OF PCT/JP00/03204 05/19/2000

**** FOREIGN APPLICATIONS VERIFIED:**

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials		ATTORNEY DOCKET NO 011712
TITLE : Method and apparatus for circuit board continuity test, tool for continuity test, and recording medium		

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.B.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drawg.	Figs. Drawg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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